

In re Patent Application of

: Atty. Docket No. POU920010165US1

Marvin J. RICH et al.

: Group Art Unit: 2825

Serial No. 10/038,209

: Confirmation No. 8625

Filed: January 2, 2002

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For:

DELAY CORRELATION ANALYSIS AND REPRESENTATION

FOR VITAL COMPLIANT VHDL MODELS

SUBMISSION OF FORMAL DRAWINGS

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Submitted herewith are 22 sheets of formal drawings containing Figures 1-22 for the above-identified application.

The Commissioner is authorized to charge any additional fees or credit any overpayment to Deposit Account No. 09-0463.

Respectfully submitted,

Date:

By:

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Registration No. 35,171

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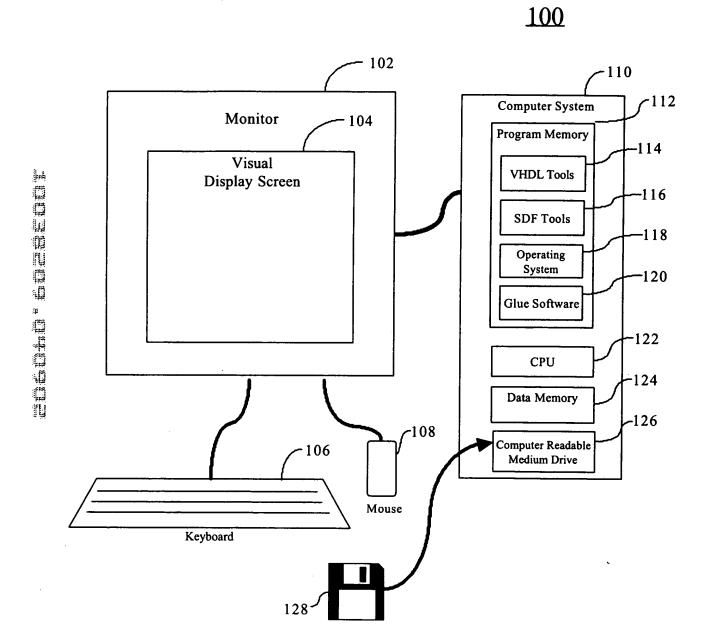


FIG 1

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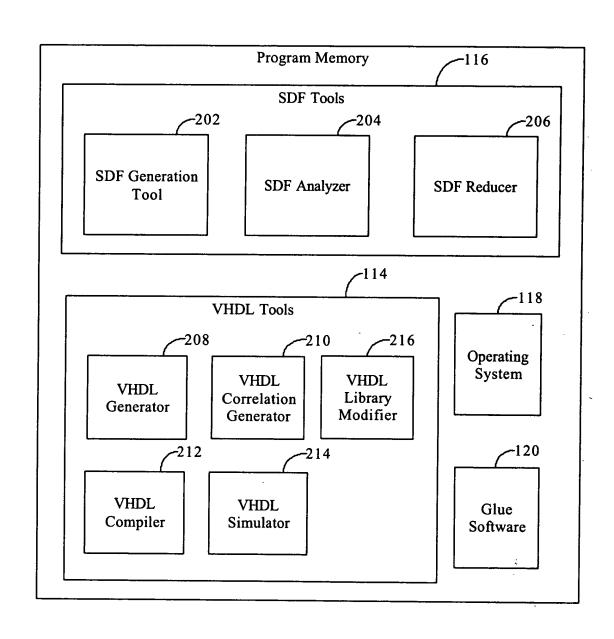


FIG 2

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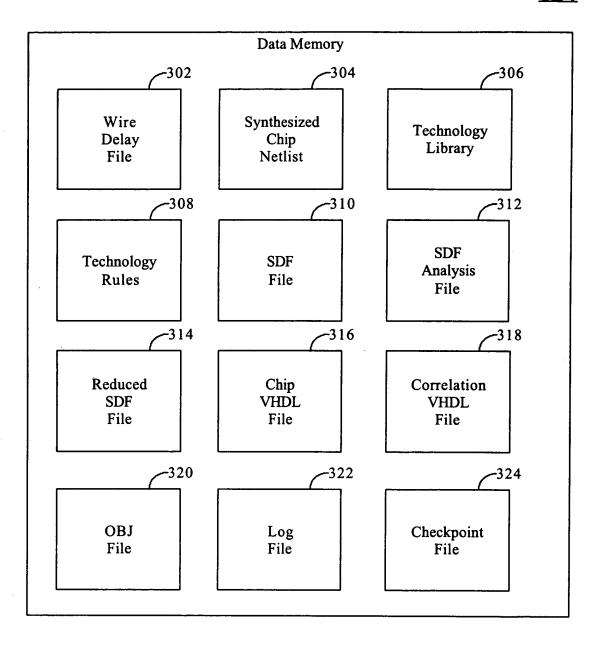


FIG 3

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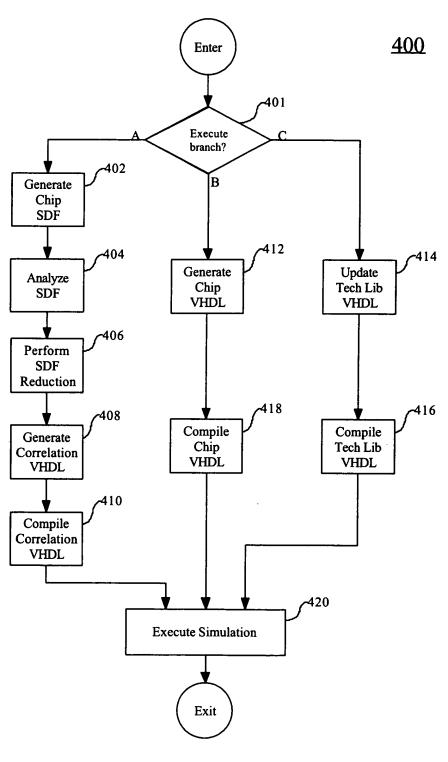


FIG 4

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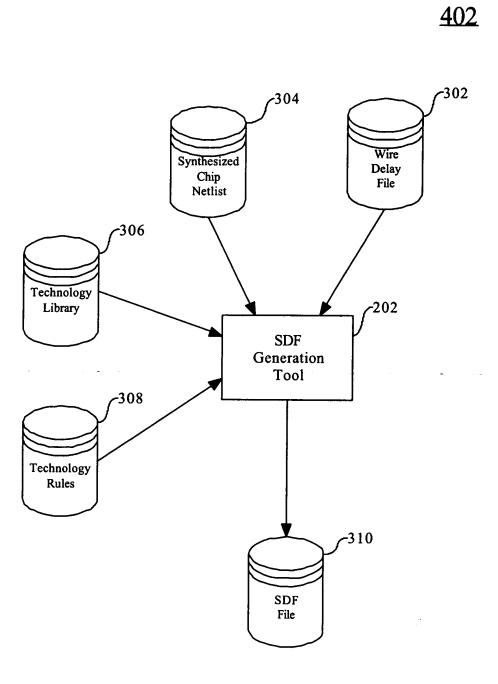
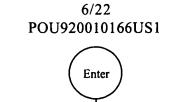


FIG 5





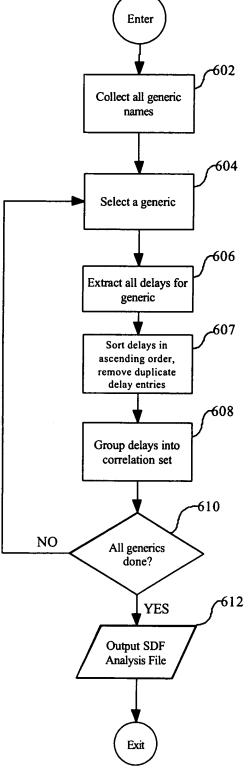


FIG 6

<u>700</u>

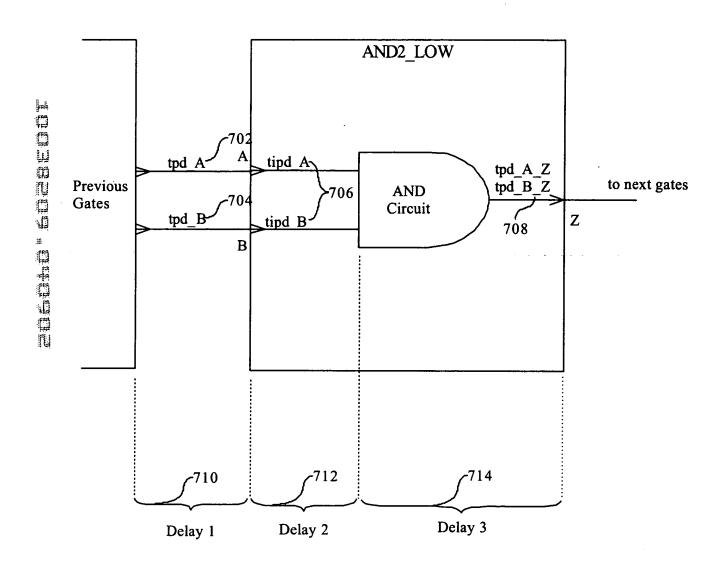


FIG 7

<u>800</u>

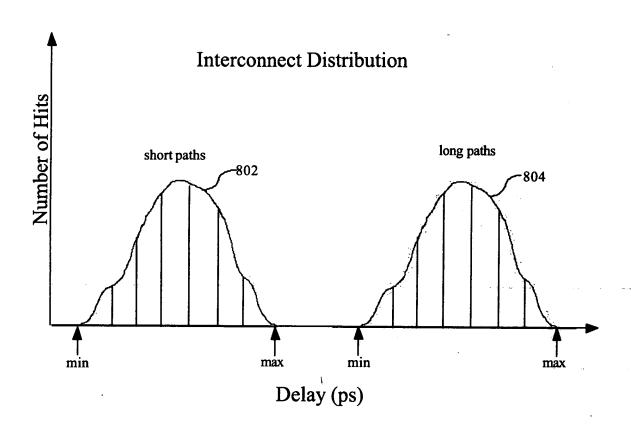


FIG 8

<u>900</u>

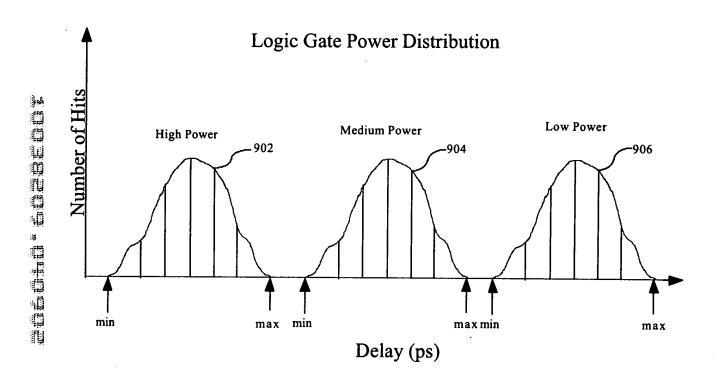


FIG 9

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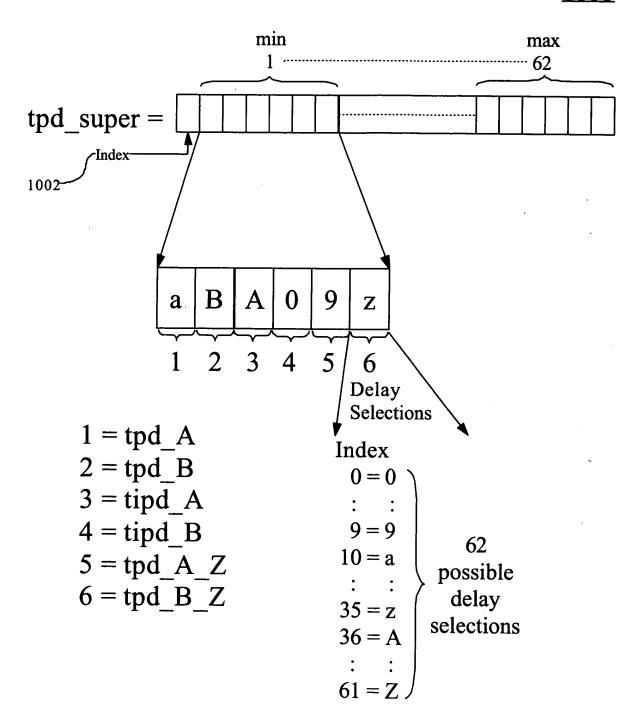


FIG 10

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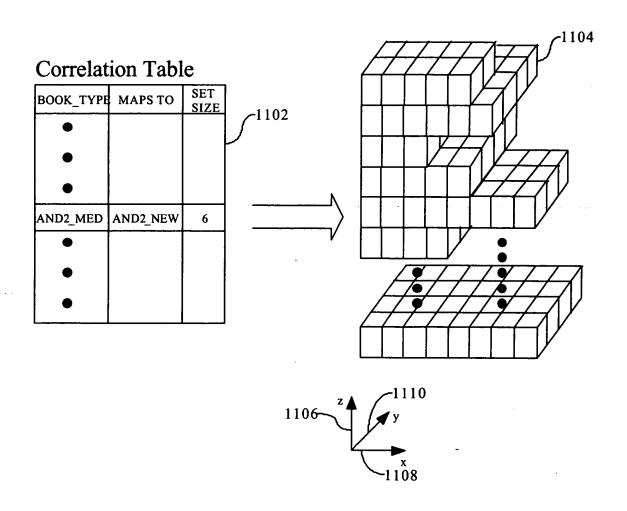


FIG 11

CHAR POSITION (0 OFFSET)	CHAR VALUE	Correlation Set (x')	Entry Position (y')	Slot Offset (x")
0	1	N/A	NΑ	N⁄Α
1	4	1	5th	1st
2	X	1	60th	2nd
3	а	1	11th	3rd
4	m	1	26th	4th
5	р	1	29th	5th
6		1	25th	6th

FIG 12

-1302

<u>z</u>

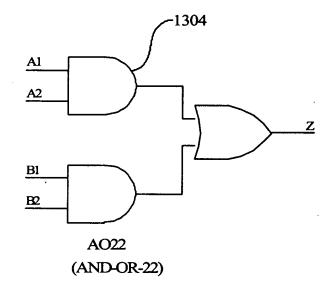


FIG 13

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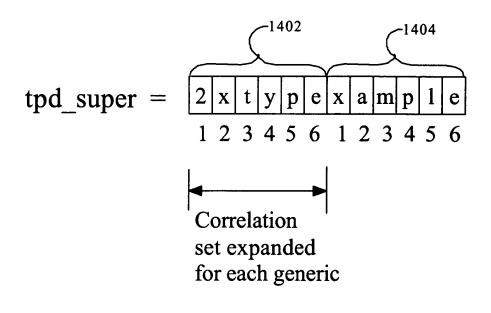
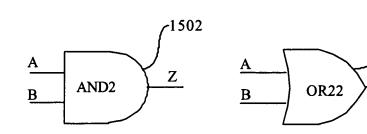
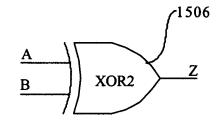
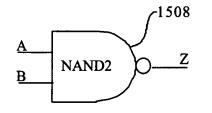


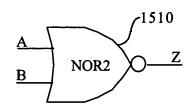
FIG 14

<u>Z</u>









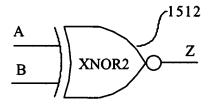
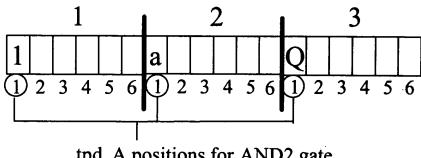


FIG 15

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tpd_A positions for AND2 gate

Encode as:

DELAY				
DELAY		1	2	3
-0.500 ns		0	0	0
-0.499 ns		0	0	1
_	ድ	_ •	. • -	
-		• .	-	
	+237.826 ns	Z	Z	Υ
1	+237.827 ns	Z	Z	Z

FIG 16

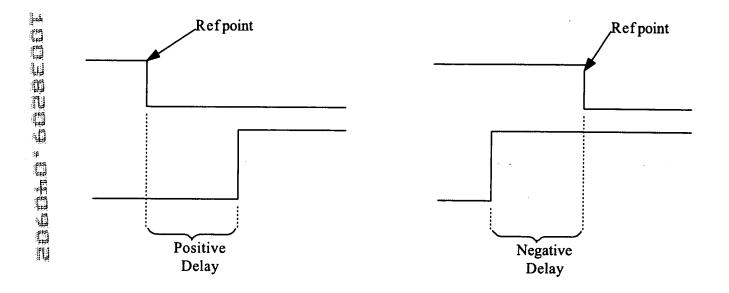


FIG 17

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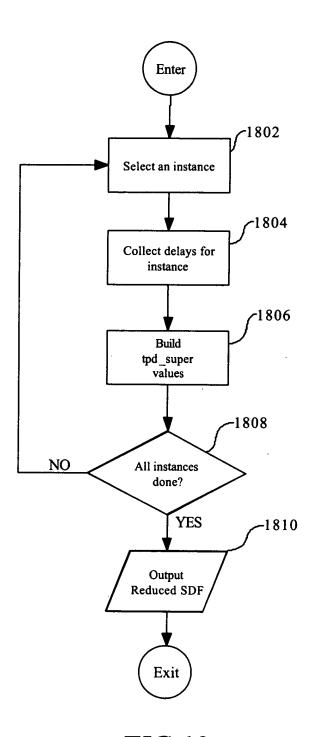


FIG 18

<u>408</u>

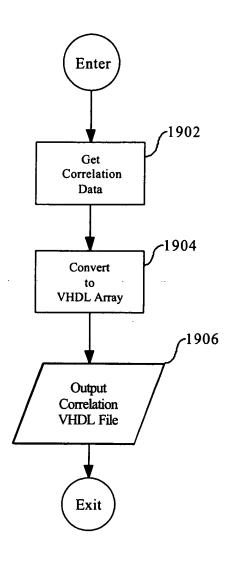


FIG 19

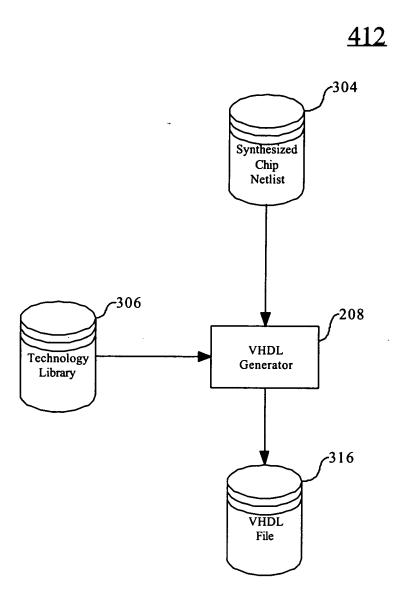


FIG 20

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<u>418</u>

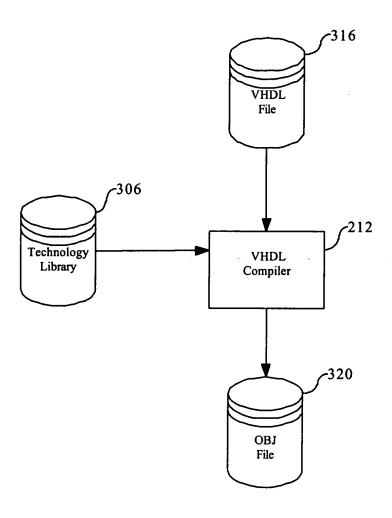
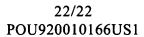


FIG 21





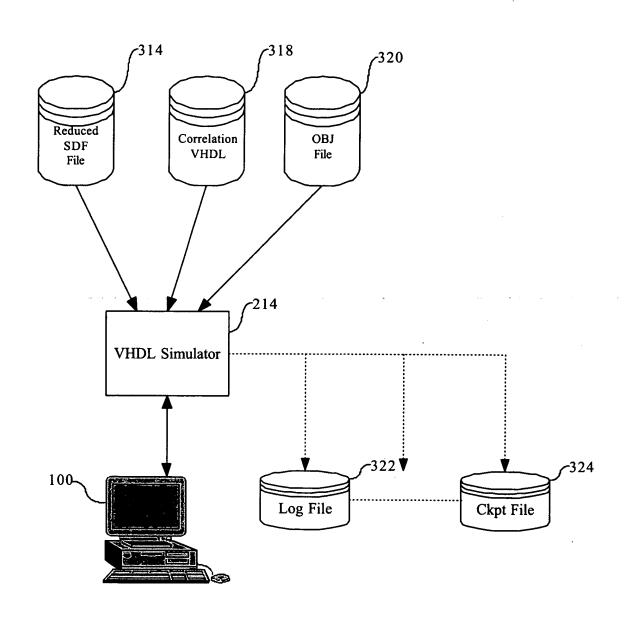


FIG 22